

HCS10FF120E0A2

1200V/10mΩ Half Bridge SiC MOSFET Module

Description

The HCS10FF120E0A2 is a Half Bridge SiC MOSFET Power Module. It integrates high performance SiC MOSFET chips designed for the applications such as Solar Inverter, UPS, Fuel cell-DC/DC converter,Energy storage Systems.



Features

- Blocking voltage:1200V
- $R_{ds(on)}=10.1\text{ m}\Omega$
- Low Switching Losses
- 175maximum junction temperature
- Thermistor inside

Applications

- Solar inverterSystems
- Fuel cell-DC/DC converter
- Uninterruptible Power Supplier
- Energy Storage Systems

Circuit diagram

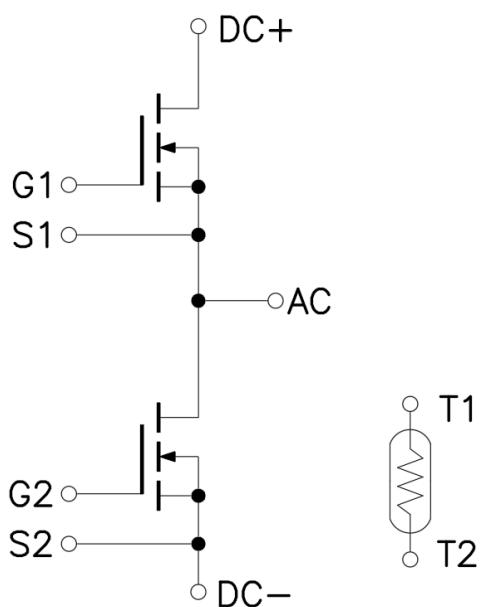


Figure 1. Out drawing & circuit diagramfor HCS10FF120E0A2

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Pin Configuration and Marking Information

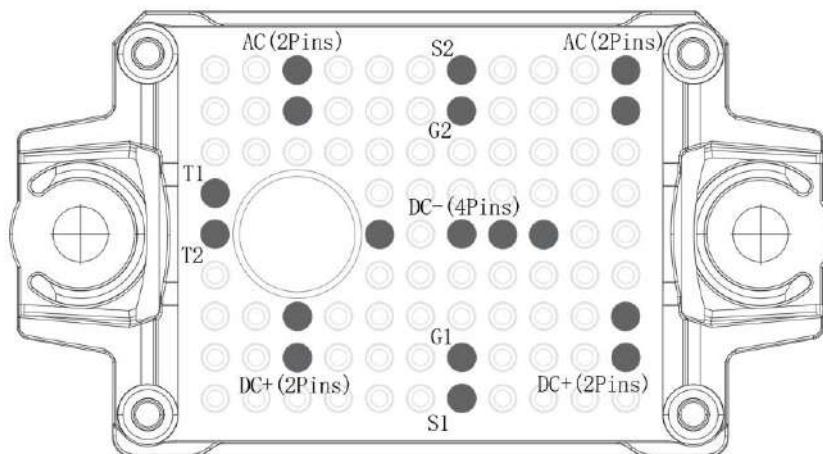


Figure 2. Pin configuration

Symbol	Description
AC	Output terminal of half bridge
S2	Low side source signal terminal
G2	Low side gate signal terminal
DC+	DC+ Bus connection
DC-	DC- Bus connection
S1	High side source signal terminal
G1	High side gate signal terminal
T1	Thermistor connection 1
T2	Thermistor connection 2

Module

Parameter	Condition	Value	Unit
Isolation Voltage	RMS, f =50Hz, t =1min	3.4	kV
Clearance	Terminal to Terminal	5	mm
	Terminal to Heatsink	10	mm
Creepage distance	Terminal to Terminal	6.3	mm
	Terminal to Heatsink	12.7	mm
Comparative Tracking Index	-	400	-
Weight	-	24	g

HCS10FF120E0A2**1200V/10mΩ Half Bridge SiC MOSFET Module****Maximum Ratings** ($T_j=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Condition	Ratings	Unit
V_{DSS}	Drain-Source Voltage	G-S Short	1200	V
$V_{GS,max}$	Gate-Source Voltage	D-S Short, Note1	-8/+19	V
I_{DS}	DC Continuous Drain Current	$T_f=75^\circ\text{C}$, $V_{GS}=18\text{V}$	100	A
I_{SD}	Source (Body Diode) Current	$T_f=75^\circ\text{C}$, with ON signal	100	A
I_{DSM}	Pulse Drain Current	$T_c=100^\circ\text{C}$, Pulse width =1ms, $V_{GS}=15\text{V}$, Note2	200	A
P_{tot}	Total Power Dissipation	$T_c=25^\circ\text{C}$	440	W
T_j	junction temperature	-	-40 to 175	$^\circ\text{C}$
T_{stg}	Storage temperature	-	-40 to 125	$^\circ\text{C}$

Note1: Recommended Operating Value, +15V/-4V

Note2: Pulse width limited by maximum junction temperature

NTC characteristics

Symbol	Parameter	Condition	Value			Unit
			Min.	Typ.	Max.	
R_{25}	Resistance	$T_c=25^\circ\text{C}$	-	5	-	k Ω
$\Delta R/R$	Deviation of R_{100}	$T_c=100^\circ\text{C}$, $R_{100}=493 \Delta R$	-5	-	5	%
P_{25}	Power dissipation	$T_c=25^\circ\text{C}$	-	-	20	mW
$B_{25/50}$	B-value	$R_2=R_{25} \exp [B_{25/50}(1/T_2 - 1/(298,15 \text{ K}))]$	-	3375	-	K
$B_{25/80}$	B-value	$R_2=R_{25} \exp [B_{25/80}(1/T_2 - 1/(298,15 \text{ K}))]$	-	3411	-	K
$B_{25/100}$	B-value	$R_2=R_{25} \exp [B_{25/100}(1/T_2 - 1/(298,15 \text{ K}))]$	-	3433	-	K

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MOSFET Electrical characteristics ($T_j=25^\circ\text{C}$ unless otherwise specified, chip)

Symbol	Item	Condition	Value			Unit	
			Min.	Typ.	Max		
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=400\mu\text{A}$	1200	-	-	V	
I_{DSS}	Zero gate voltage drain Current	$V_{\text{DS}}=1200\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-2	200	μA	
$V_{\text{GS}(\text{th})}$	Gate-source threshold Voltage	$I_D=40\text{mA}$, $V_{\text{DS}}=V_{\text{GS}}$	$T_j=25^\circ\text{C}$	1.8	.61	V	
			$T_j=175^\circ\text{C}$	-	.9	V	
$I_{\text{GSS}+}$	Gate-Source Leakage Current	$V_{\text{GS}}=15\text{V}$, $V_{\text{DS}}=0\text{V}$, $T_j=25^\circ\text{C}$	-	10	800	nA	
$I_{\text{GSS}-}$			$V_{\text{GS}}=-4\text{V}$, $V_{\text{DS}}=0\text{V}$, $T_j=25^\circ\text{C}$	-800	-10	nA	
$R_{\text{DS}(\text{on})}$ (Chip)	Static drain-source On-state resistance	$I_D=100\text{A}$, $V_{\text{GS}}=15\text{V}$	$T_j=25^\circ\text{C}$	-	10.1	$\text{m}\Omega$	
			$T_j=175^\circ\text{C}$	-	18.8	$\text{m}\Omega$	
$V_{\text{DS}(\text{on})}$ (Chip)	Static drain-source On-state Voltage	$I_D=100\text{A}$, $V_{\text{GS}}=15\text{V}$	$T_j=25^\circ\text{C}$	-	1.01	V	
			$T_j=175^\circ\text{C}$	-	1.88	V	
C_{iss}	Input Capacitance	$V_{\text{DS}}=1000\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=100\text{kHz}$, $T_j=25^\circ\text{C}$	-	9200	-	pF	
C_{oss}	Output Capacitance		-	475	-	pF	
C_{rss}	Reverse transfer Capacitance		-	29	-	pF	
Q_G	Total gate charge	$V_{\text{DD}}=800\text{V}$, $I_D=100\text{A}$, $V_{\text{GS}}=-4/+15\text{V}$	-	398	-	nC	
R_{Gint}	Internal Gate Resistance	$T_j=25^\circ\text{C}$	-	1.5	-	Ω	
$t_{\text{d}(\text{on})}$	Turn-on delay time	$V_{\text{DD}}=600\text{V}$ $I_D=100\text{A}$ $V_{\text{GS}}=+15/-4\text{V}$ $R_{\text{gon}}=R_{\text{goff}}=5.1\boxtimes$ Inductive load switching operation	$T_j=25^\circ\text{C}$	-	23	ns	
			$T_j=150^\circ\text{C}$	-	21		
t_r	Rise time		$T_j=25^\circ\text{C}$	-	18	ns	
			$T_j=150^\circ\text{C}$	-	15		
$t_{\text{d}(\text{off})}$	Turn-off delay time		$T_j=25^\circ\text{C}$	-	33	ns	
			$T_j=150^\circ\text{C}$	-	21		
t_f	Fall time		$T_j=25^\circ\text{C}$	-	15	ns	
			$T_j=150^\circ\text{C}$	-	14		
E_{on}	Turn-on power dissipation		$T_j=25^\circ\text{C}$	-	1050	μJ	
			$T_j=150^\circ\text{C}$	-	1455		
E_{off}	Turn-off power dissipation		$T_j=25^\circ\text{C}$	-	245	μJ	
			$T_j=150^\circ\text{C}$	-	350		
$R_{\text{th(j-c)}}$	FET Thermal Resistance	Junction to Case/MOSFET	-	0.34	-	K/W	
$R_{\text{th(c-f)}}$	Contact thermal resistance	With thermal conductive grease /MOSFET	-	0.15	-	K/W	

Assumes Thermal Conductivity of grease is 2.8 W/m · K and thickness is 50um.

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Body Diode Electrical characteristics ($T_j=25^\circ\text{C}$ unless otherwise specified, chip: Target)

Symbol	Item	Condition	Value			Unit
			Min.	Typ.	Max	
V _{SD}	Body Diode Forward Voltage	V _{GS} =-4V	-	4.6	-	V
		I _{SD} =100A	-	4.1	-	
T _{rr}	Reverse recovery time	V _{RR} =600V, I _D =100A	-	23	-	ns
		V _{GS} =+15/-4V	-	24	-	
Q _{rr}	Reverse recovery charge	R _{gon} =R _{goff} =5.1Ω	-	0.44	-	μC
		Inductive load	-	0.76	-	
E _{rr}	Diode switching power dissipation	T _j =25°C	-	450	-	μJ
		T _j =150°C	-	890	-	

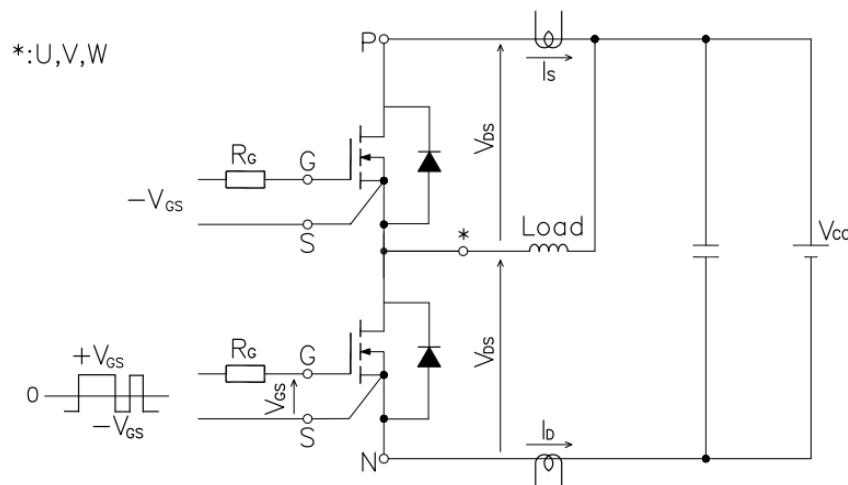
Test Conditions

Figure 3. Switching time measure circuit

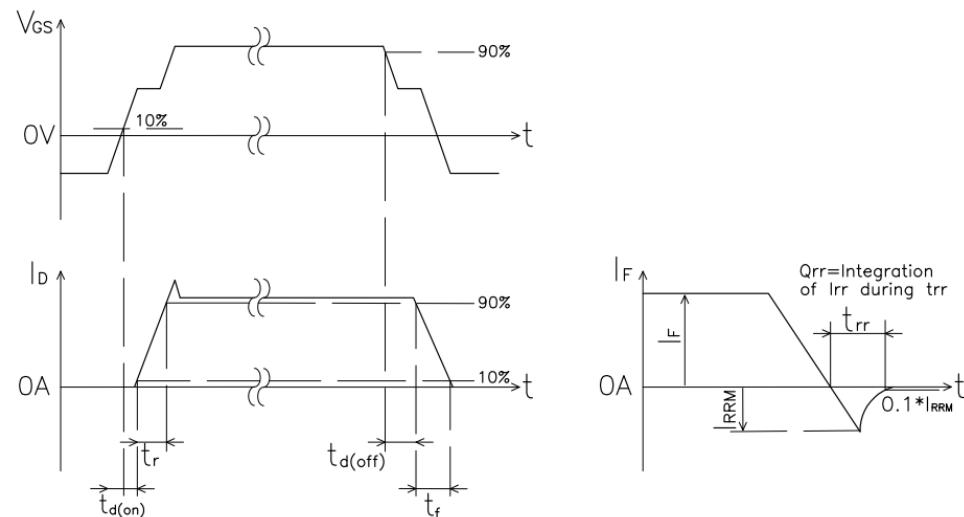
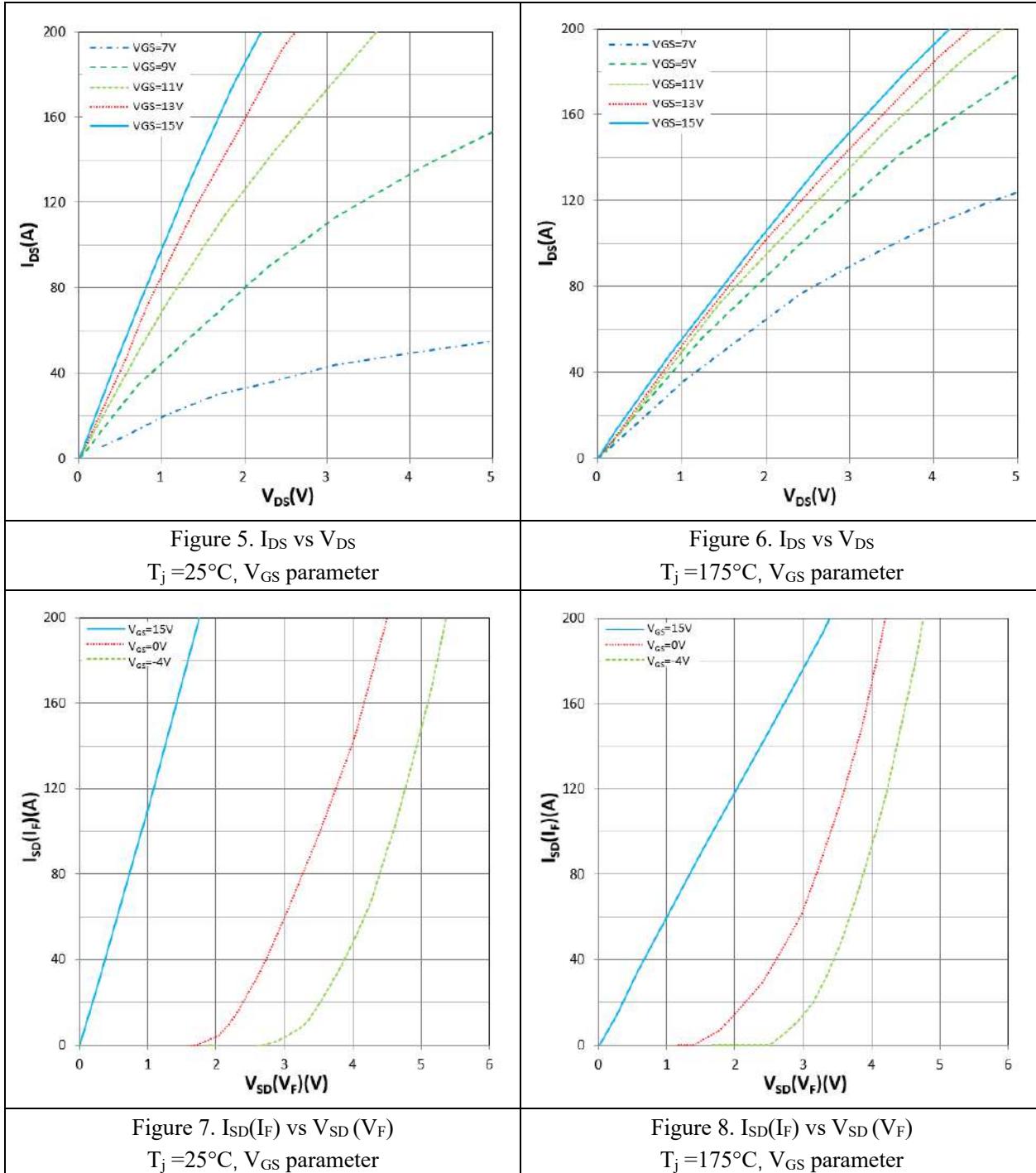
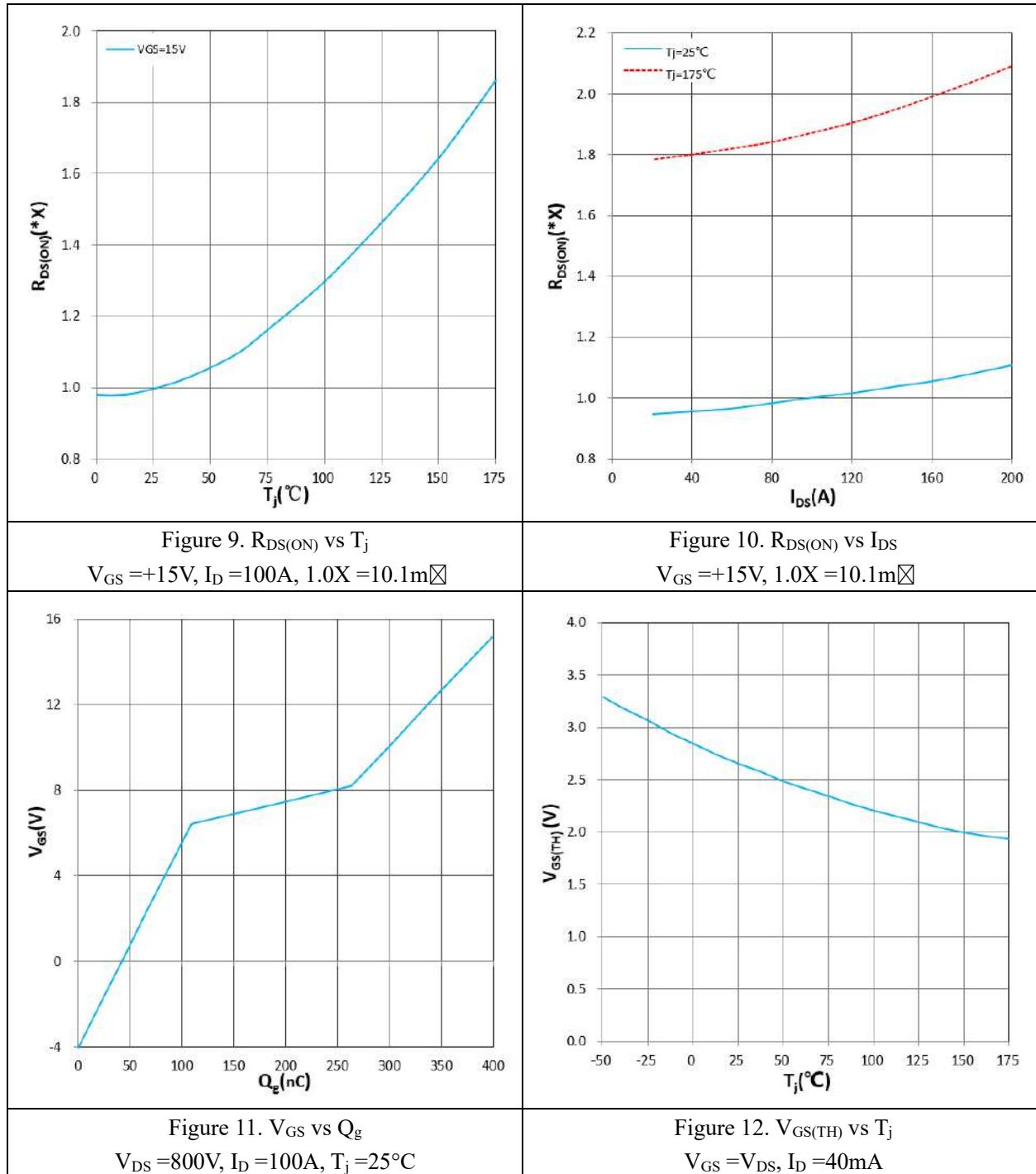


Figure 4. Switching time definition

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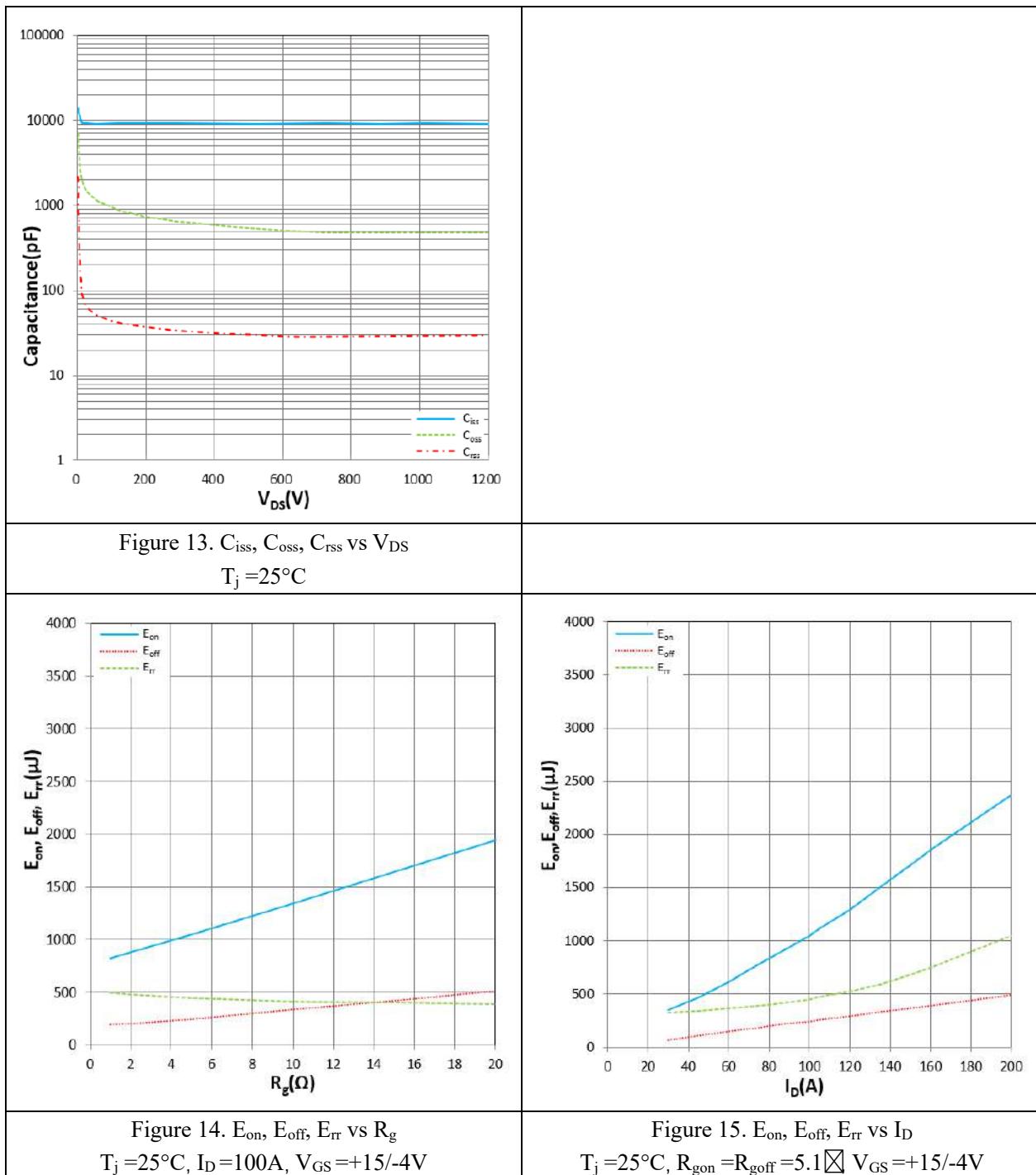
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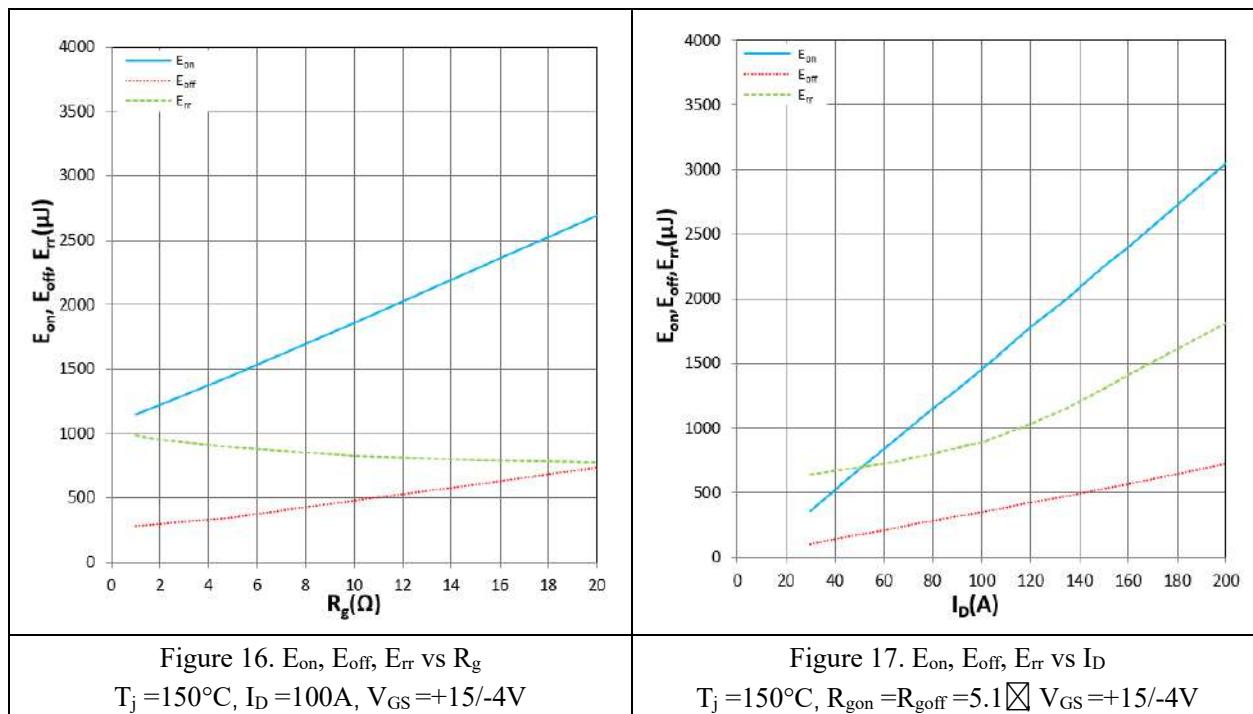
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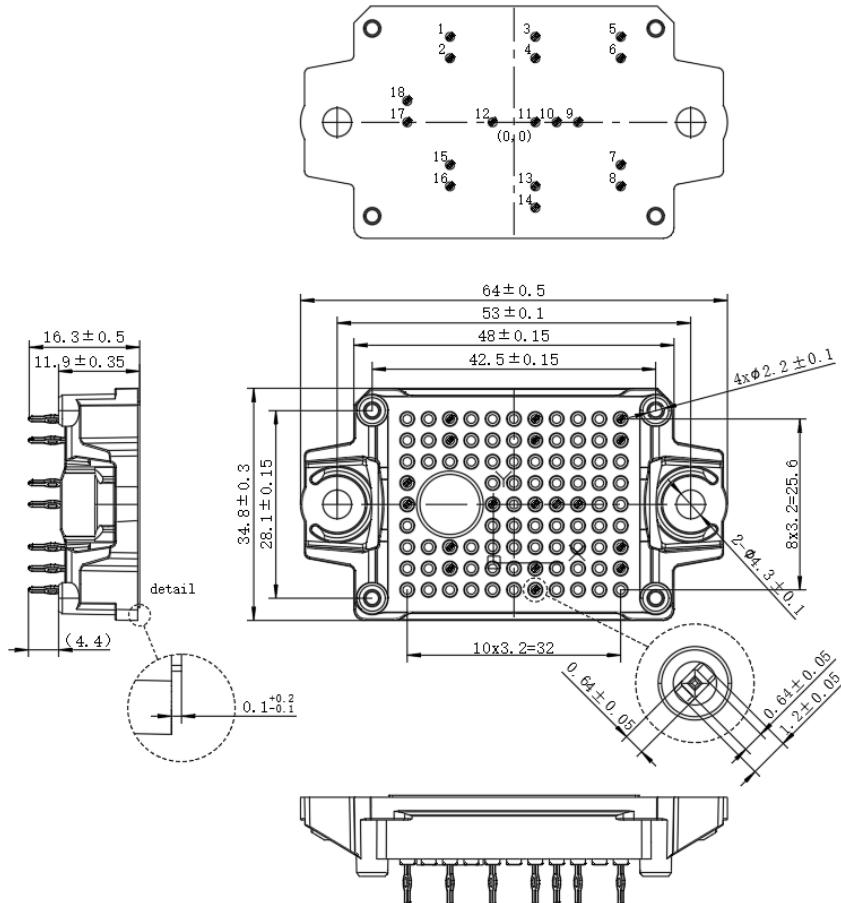
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Package dimensions



Pin Table		
Pin	X	Y
1	-9.6	12.8
2	-9.6	9.6
3	3.2	12.8
4	3.2	9.6
5	16	12.8
6	16	9.6
7	16	-6.4
8	16	-9.6
9	9.6	0
10	6.4	0
11	3.2	0
12	-3.2	0
13	3.2	-9.6
14	3.2	-12.8
15	-9.6	-6.4
16	-9.6	-9.6
17	-16	0
18	-16	3.2

IMPORTANT NOTICE:

This product data sheet describes the characteristics of this product for which a warranty is granted. Any such warranty is granted exclusively under the terms and conditions of the supply agreement. There will be no guarantee or of any kind for the product and its characteristics.

The data contained in this document is exclusively intended for technically trained staff. You and your technical departments will have to evaluate the product's suitability for the intended application and the completeness of the product data concerning such application.

Due to technical requirements, our product may contain dangerous substances. For information on the types in question, please contact the sales staff responsible for you.

Changes to this product data sheet are reserved.

Please contact the sales staff (sales@hiitio.com) for further information on the product, technology, delivery terms, conditions and prices.

Instruction note

Naming rules for power module product models (Industrial module)

Product Model							
	HC	S	10	FF	120	E0	A2
Hecheng Code							
Module type	G : IGBT module D : FRD module S : SiC module H : Si/SiC hybrid						
On-state resistance (mΩ)	01~80						
Topology structure	FZ : A switch unit FS : Three phase F3L : Three level FD : Braking Circuit FP : Rectification+Inverter+Control move AL : ANPC	FF : Half bridge F4 : H Bridge DF : Boost Circuit CL : Chopper					
Voltage level (x10) (V)	650~2200						
Packaging form+features (A...Z)	A1: 34 mm B1: Easy 1B B2: Easy 2B... D1: Flow0 E0 : E0 E3: ED3 E6 : EPM2 E9 : ED3H	A2: 62 mm B1A B3: Easy 3B... D2: Flow1 E1: Econo 2... E4 : E4 E7 : EPM3 F0 : F0	B1B... D3: Flow2 E2: E2 E5 : ED3S E8 : EconoPIM3 P2 : EPM2				
Feature :A:	Special Code	Nil: Standard					

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