

HCS36FS140E0Q1

1400V/36mΩ3 PhaseSiC MOSFETModule

Description

The HCS36FS140E0Q1 is a 3PhaseSiC MOSFET Power Module. It integrates high performanceSiC MOSFET chips designed for the applications such as DC/DC Converter, Motor converter, UPS, High Frequency Switching application.



Features

- Blocking voltage:1400V
- $R_{ds(on)}=36m\Omega$
- Low Switching Losses
- 175 C maximum junction temperature
- Thermistor inside

Applications

- DC/DC converter
- Motor converter
- Uninterruptible Power Supplier
- High Frequency Switching application

Circuit diagram

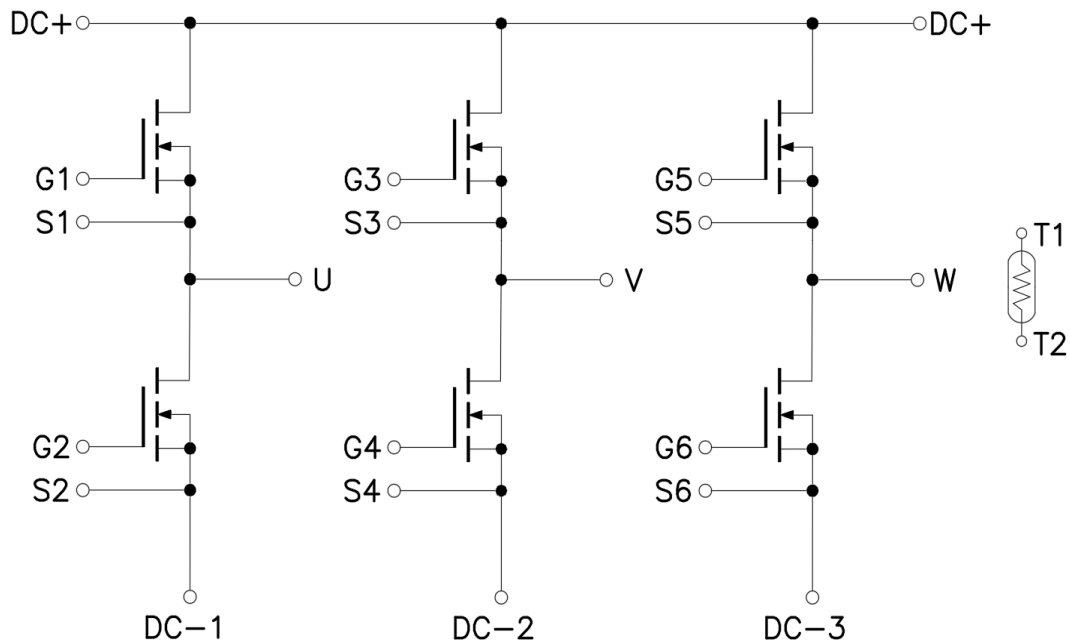


Figure 1. Out drawing & circuit diagram for HCS36FS140E0Q1

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Pin Configuration and Marking Information

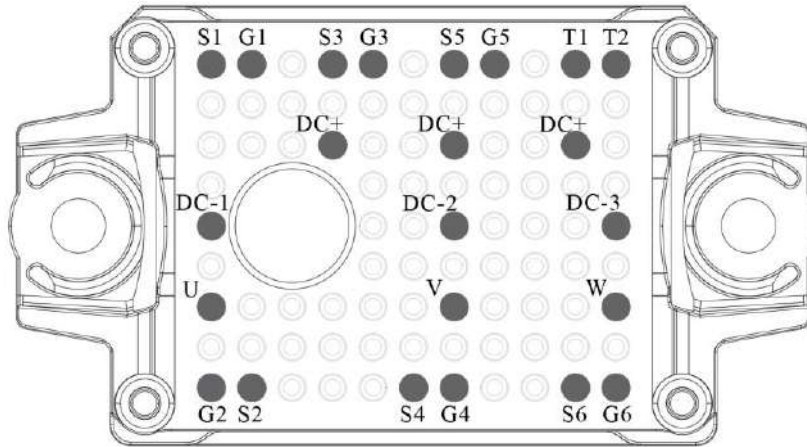


Figure 2. Pin configuration

Symbol	Description
U,V,W	Output terminal of 3 Phase
S2,S4,S6	Low side source signal terminal
G2,G4,G6	Low side gate signal terminal
DC+(3Pin)	DC+ Bus connection
DC-1,2,3	DC- Bus connection
S1,S3,S5	High side source signal terminal
G1,G3,G5	High side gate signal terminal
T1	Thermistor connection 1
T2	Thermistor connection 2

Module

Parameter	Conditions	Value	Unit
Isolation Voltage	RMS, f =50Hz, t =1min	3.4	kV
Clearance	Terminal to Terminal	5	mm
	Terminal to Heatsink	10	mm
Creepage distance	Terminal to Terminal	6.3	mm
	Terminal to Heatsink	12.7	mm
Comparative Tracking Index	-	400	-
Weight	-	24	g

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Maximum Ratings (T_j=25°C unless otherwise specified)

Symbol	Parameter	Condition	Ratings	Unit
V _{DSS}	Drain-Source Voltage	G-S Short	1400	V
V _{DS nom}	Continuous Operating DC Voltage	Not include surge voltage	1100	V
V _{GSS}	Gate-Source Voltage(+)	D-S Short	20	V
V _{GSS}	Gate-Source Voltage(-)	D-S Short	-5	V
V _{GSSSurge}	G-S Voltage(t _{surge} <300nsec)	D-S Short, Note1	-10 to 25	V
I _{DS}	DC Continuous Drain Current	T _r =145°C, Note2	25	A
I _{SD}	Source (Body Diode) Current	T _r =145°C, with ON signal	25	A
I _{DP}	Drain Pulse Current, Peak	Less than 1ms, Note3	50	A
T _j	junction temperature	-	-40 to 175	°C
T _{stg}	Storage temperature	-	-40 to 125	°C

Note1: Recommended Operating Value, +20V/-5V;+18V/-4V;+15V/-4V

Note2: Case temperature(T_c) is defined on the surface of AMB substrate bottom just under the chips

Note3: Pulse width limited by maximum junction temperature

NTC characteristics

Symbol	Parameter	Condition	Value			Unit
			Min.	Typ.	Max.	
R ₂₅	Resistance	T _c =25°C	-	5	-	kΩ
R/R	Deviation of R ₁₀₀	T _c =100°C, R ₁₀₀ =493Ω	-5	-	5	%
P ₂₅	Power dissipation	T _c =25°C	-	-	20	mW
B _{25/50}	B-value	R ₂ =R ₂₅ exp [B _{25/50} (1/T ₂ - 1/(298,15 K))]	-	3375	-	K
B _{25/80}	B-value	R ₂ =R ₂₅ exp [B _{25/80} (1/T ₂ - 1/(298,15 K))]	-	3411	-	K
B _{25/100}	B-value	R ₂ =R ₂₅ exp [B _{25/100} (1/T ₂ - 1/(298,15 K))]	-	3433	-	K

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MOSFET Electrical characteristics (T_j=25°C unless otherwise specified, chip)

Symbol	Item	Condition	Value			Unit	
			Min.	Typ.	Max		
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =1mA	1400	-	-	V	
I _{DSS}	Zero gate voltage drain Current	V _{DS} =1400V, V _{GS} =0V	-	-	300	μA	
V _{GS(th)}	Gate-Source threshold Voltage	I _D =20mA, V _{DS} =V _{GS}	2.0	2.5	4.0	V	
I _{GSS+}	Gate-Source Leakage Current	V _{GS} =20V, V _{DS} =0V, T _j =25°C	-	-	200	nA	
I _{GSS-}		V _{GS} =-5V, V _{DS} =0V, T _j =25°C	-200	-	-	nA	
R _{DS(on)} (Chip)	Static drain-source On-state resistance	I _D =25A, V _{GS} =20V	T _j =25°C	-	36	45	mΩ
			T _j =175°C	-	70	-	mΩ
V _{DS(on)} (Chip)	Static drain-source On-state Voltage	I _D =25A, V _{GS} =20V	T _j =25°C	-	0.9	1.13	V
			T _j =175°C	-	1.75	-	V
C _{iss}	Input Vapacitance	V _{DS} =1000V, V _{GS} =0V, f =200kHz	-	3192	-	pF	
C _{oss}	Output Vapacitance		-	132	-	pF	
C _{rss}	Reverse transfer Capacitance		-	7	-	pF	
Q _G	Total gate charge	V _{DD} =800V, I _D =40A, V _{GS} =+20/-5V	-	118	-	nC	
R _{Gint}	Internal Gate Resistance	T _j =25°C	-	1.9	-	Ω	
t _{d(on)}	Turn-on delay time	V _{DD} =800V I _D =25A V _{GS} =+18/-4V R _G =5.1Ω Inductive load switching operation	T _j =25°C	-	19	-	ns
			T _j =150°C	-	20	-	
t _r	Rise time		T _j =25°C	-	9	-	ns
			T _j =150°C	-	8	-	
t _{d(off)}	Turn-off delay time		T _j =25°C	-	26	-	ns
			T _j =150°C	-	33	-	
t _f	Fall time		T _j =25°C	-	13	-	ns
			T _j =150°C	-	13	-	
E _{on}	Turn-on power dissipation		T _j =25°C	-	225	-	μJ
			T _j =150°C	-	313	-	
E _{off}	Turn-off power dissipation		T _j =25°C	-	312	-	μJ
			T _j =150°C	-	20	-	
R _{th(j-c)}	FET Thermal Resistance	Junction to Case/MOSFET	-	0.52	-	K/W	
R _{th(c-f)}	Contact thermal resistance	With thermal conductive grease /MOSFET	-	0.15	-	K/W	

Assumes Thermal Conductivity of grease is 2.8 W/m · K and thickness is 50um.

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Body Diode Electrical characteristics ($T_j=25^\circ\text{C}$ unless otherwise specified, chip: Target)

Symbol	Item	Condition	Value			Unit	
			Min.	Typ.	Max		
V_{SD}	Body Diode Forward Voltage	$V_{GS} = -5\text{V}$ $I_{SD} = 25\text{A}$	$T_j = 25^\circ\text{C}$	-	3.9	-	V
			$T_j = 175^\circ\text{C}$	-	3.4	-	
T_{rr}	Reverse recovery time	$V_{DD} = 800\text{V}$ $I_D = 25\text{A}$	$T_j = 25^\circ\text{C}$	-	27	-	ns
			$T_j = 150^\circ\text{C}$	-	28	-	
Q_{rr}	Reverse recovery charge	$V_{GS} = +18/-4\text{V}$ $R_G = 5.1\ \Omega$	$T_j = 25^\circ\text{C}$	-	0.66	-	μC
			$T_j = 150^\circ\text{C}$	-	1.77	-	
E_{rr}	Diode switching power dissipation	Inductive load switching operation	$T_j = 25^\circ\text{C}$	-	380	-	μJ
			$T_j = 150^\circ\text{C}$	-	848	-	

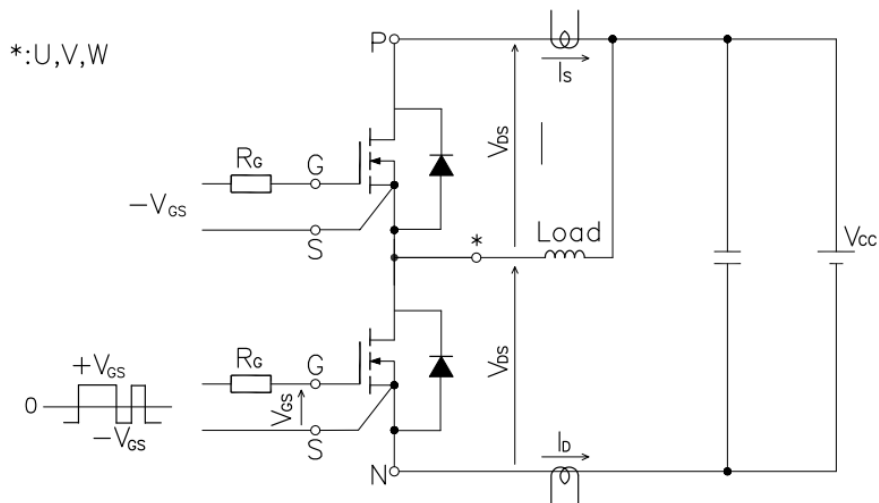
Test Conditions


Figure 3. Switching time measure circuit

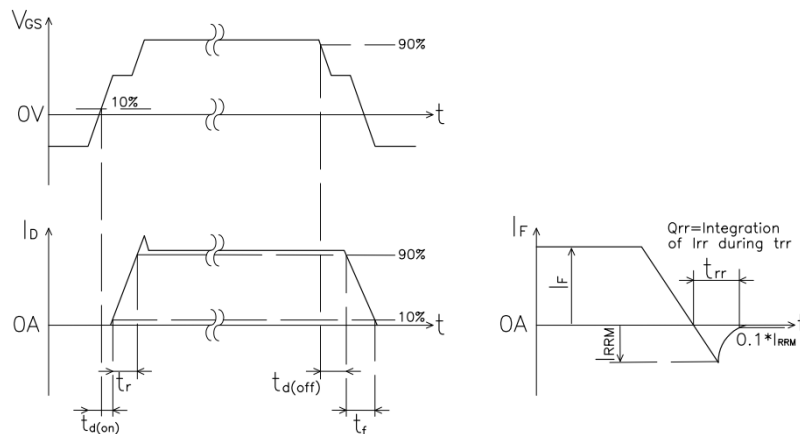


Figure 4. Switching time definition

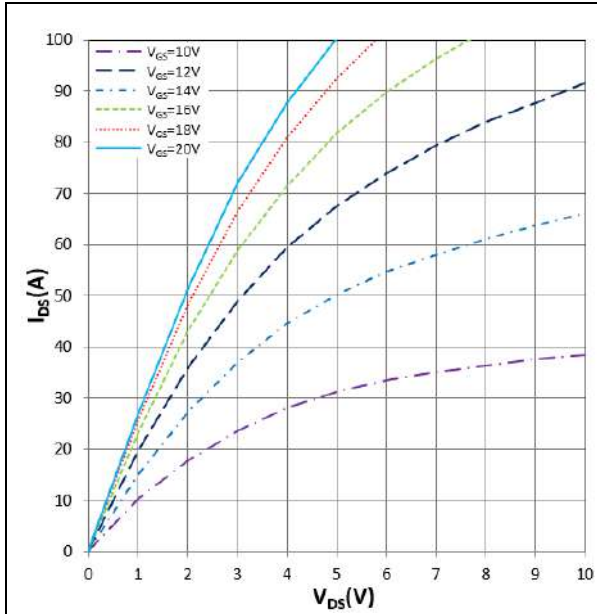
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Figure 5. I_{D_S} vs V_{D_S}
 $T_j = 25^\circ\text{C}$, V_{G_S} parameter

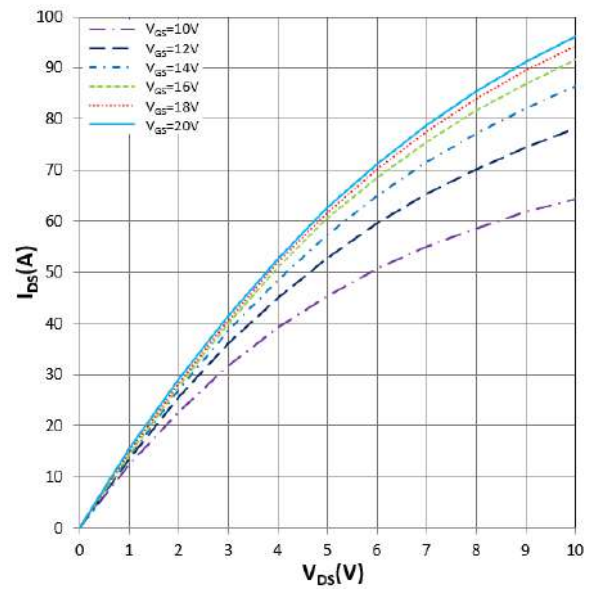


Figure 6. I_{D_S} vs V_{D_S}
 $T_j = 175^\circ\text{C}$, V_{G_S} parameter

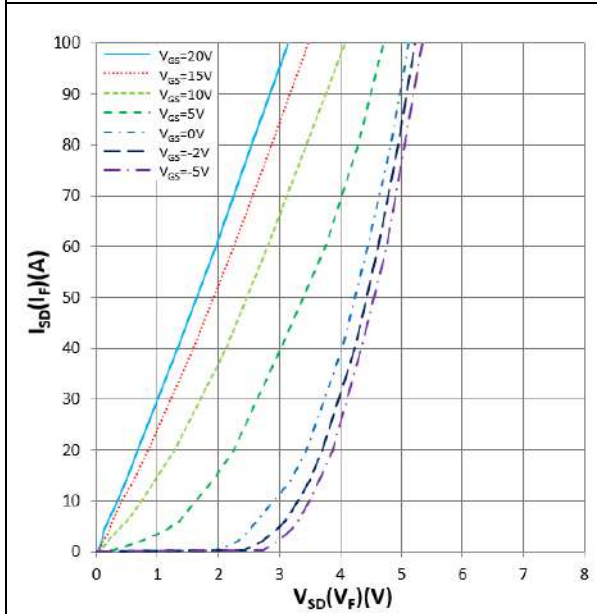


Figure 7. $I_{SD}(I_F)$ vs $V_{SD}(V_F)$
 $T_j = 25^\circ\text{C}$, V_{G_S} parameter

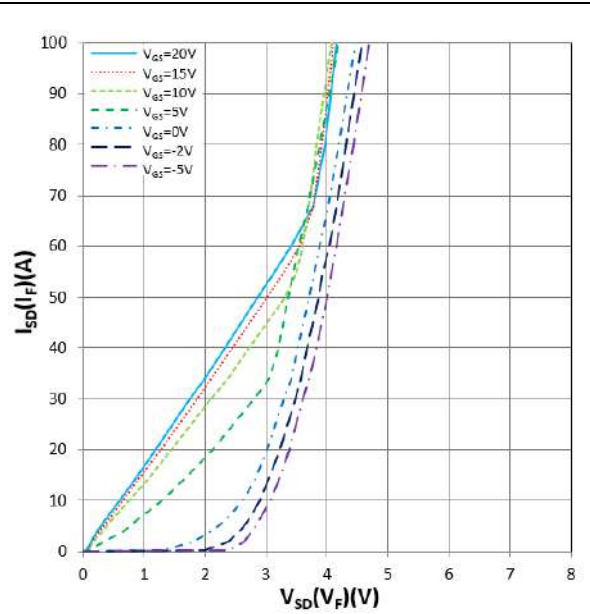


Figure 8. $I_{SD}(I_F)$ vs $V_{SD}(V_F)$
 $T_j = 175^\circ\text{C}$, V_{G_S} parameter

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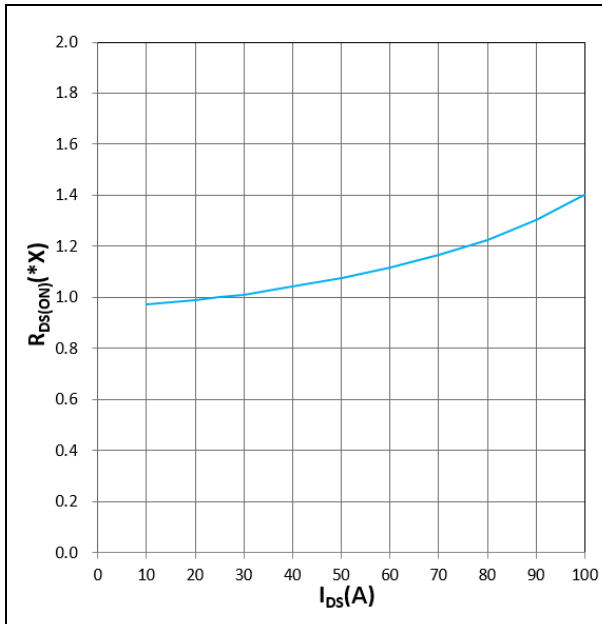


Figure 9. $R_{DS(ON)}$ vs I_{DS}
 $V_{GS} = 20V$, $1.0X = 36m\Omega$

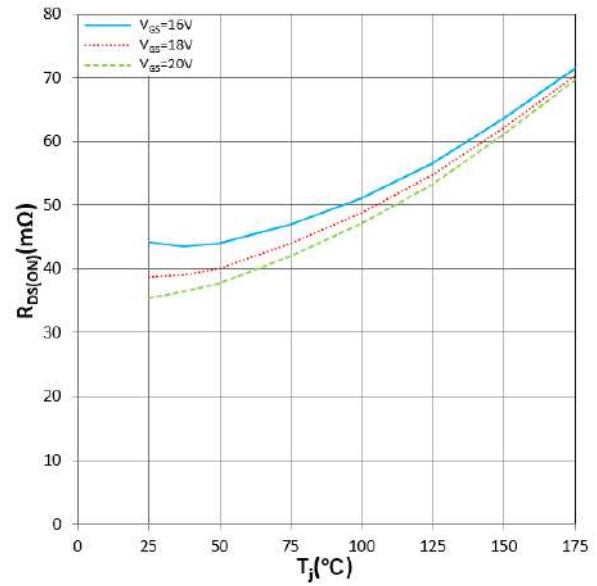


Figure 10. $R_{DS(ON)}$ vs T_J
 $I_D = 25A$

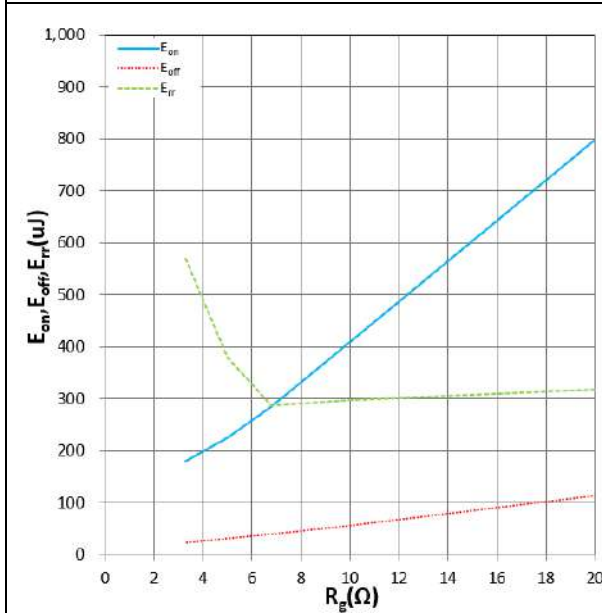


Figure 11. E_{on} , E_{off} , E_{rr} vs R_G
 $T_J = 25^\circ C$, $V_{DD} = 800V$, $I_D = 25A$, $V_{GS} = +18/-4V$

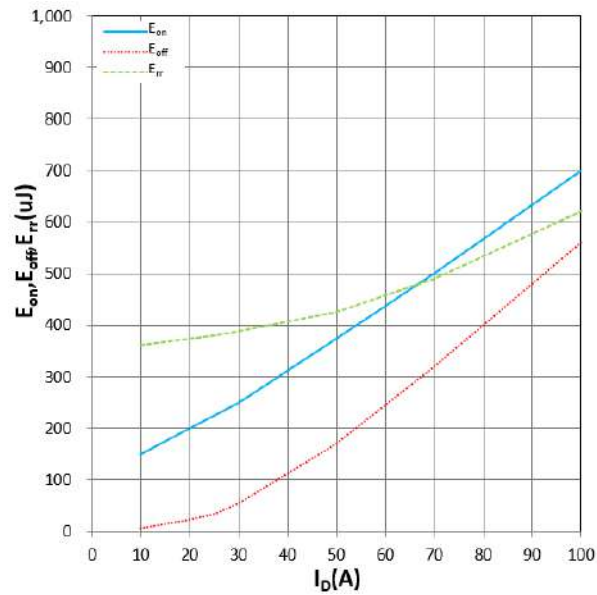


Figure 12. E_{on} , E_{off} , E_{rr} vs I_D
 $T_J = 25^\circ C$, $V_{DD} = 800V$, $R_G = 5.1\Omega$, $V_{GS} = +18/-4V$

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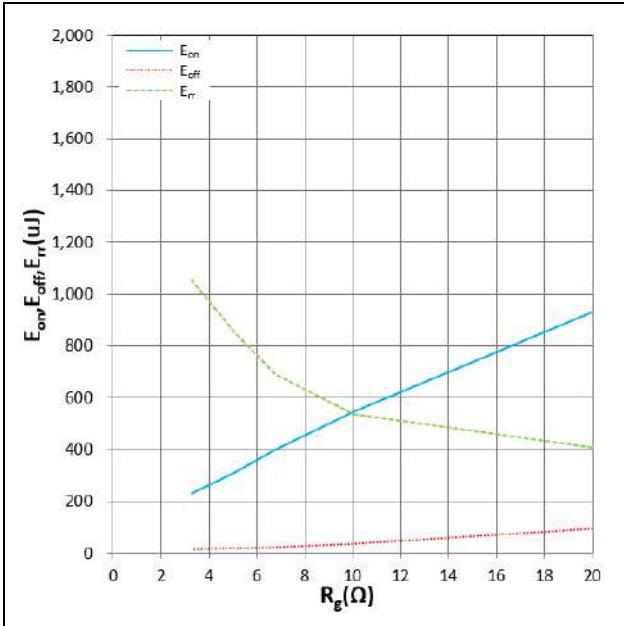


Figure 13. E_{on} , E_{off} , E_{rr} vs R_G
 $T_j = 150^{\circ}C$, $V_{DD} = 800V$, $I_D = 25A$, $V_{GS} = +18/-4V$

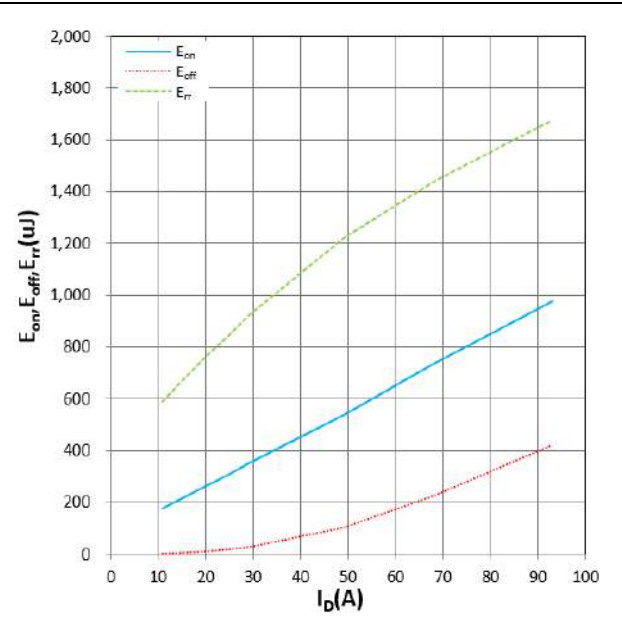


Figure 14. E_{on} , E_{off} , E_{rr} vs I_D
 $T_j = 150^{\circ}C$, $V_{DD} = 800V$, $R_G = 5.1\Omega$, $V_{GS} = +18/-4V$

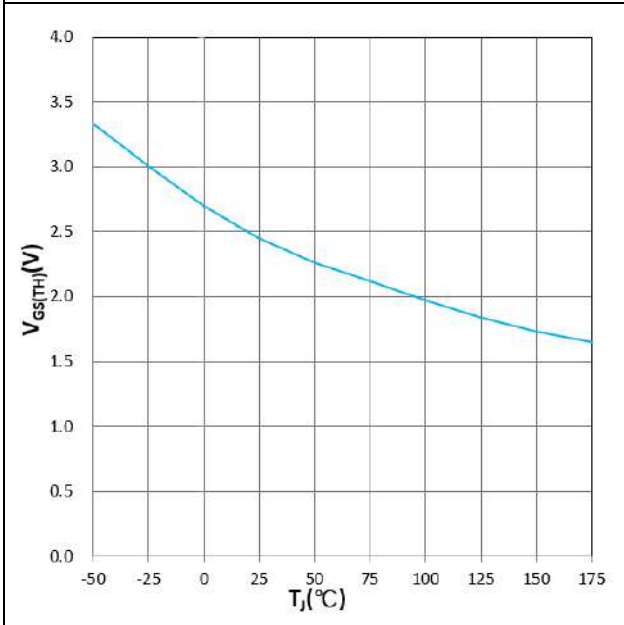


Figure 15. $V_{GS(TH)}$ vs T_j
 $V_{DS} = V_{GS}$, $I_{DS} = 20mA$

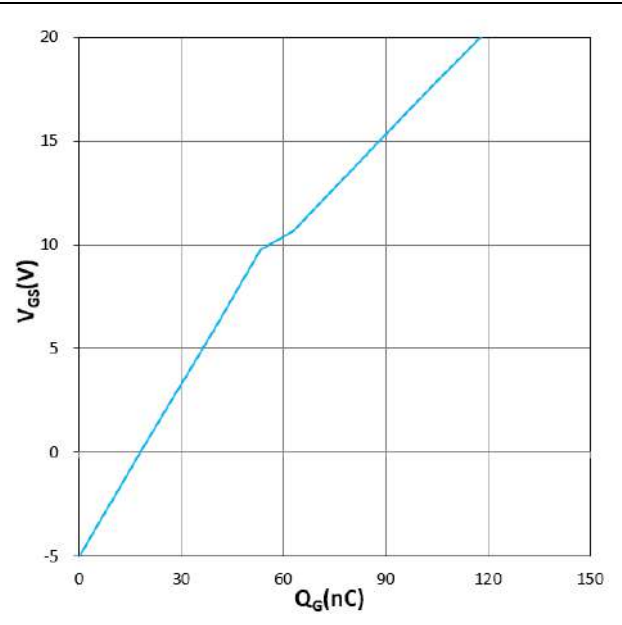


Figure 16. V_{GS} vs Q_G
 $V_{DD} = 800V$, $I_D = 40A$

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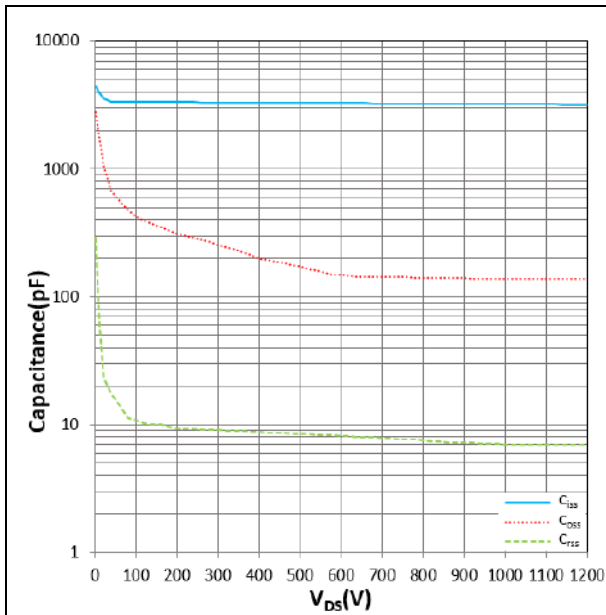


Figure 17. C_{iss} , C_{oss} , C_{rss} vs V_{DS}
 $T_j = 25^\circ\text{C}$

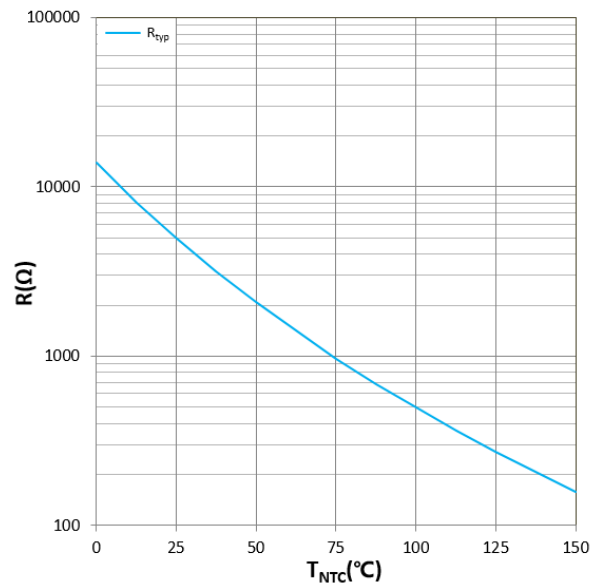
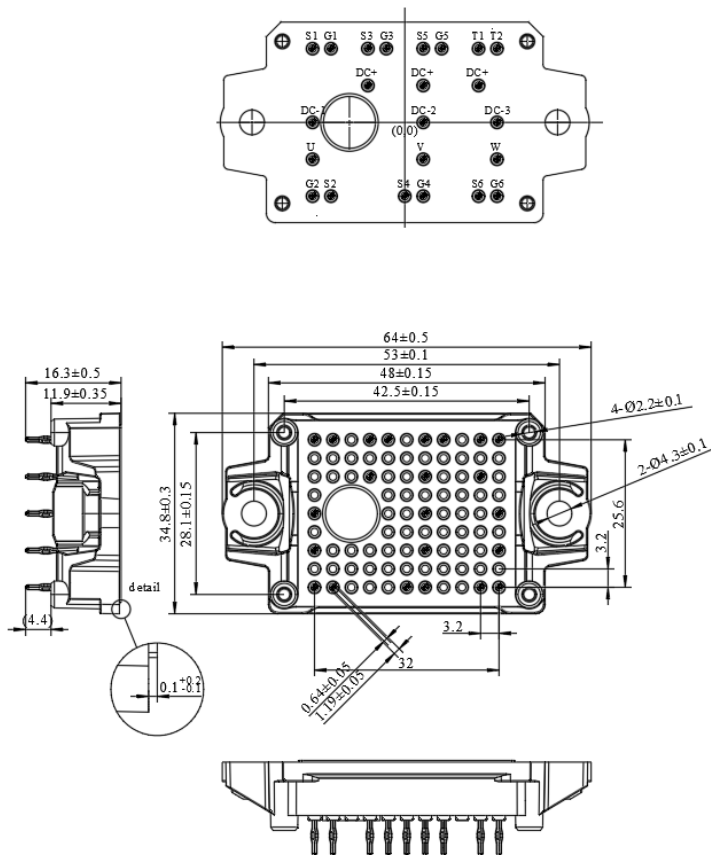


Figure 18. R vs T_{NTC}

Package dimensions



Pin table

Pin	X	Y
T2	16	12.8
T1	12.8	12.8
G5	6.4	12.8
S5	3.2	12.8
G3	-3.2	12.8
S3	-6.4	12.8
G1	-12.8	12.8
S1	-16	12.8
DC+	12.8	6.4
DC+	3.2	6.4
DC+	-6.4	6.4
DC-3	16	0
DC-2	3.2	0
DC-1	-16	0
W	16	-6.4
V	3.2	-6.4
U	-16	-6.4
G6	16	-12.8
S6	12.8	-12.8
G4	3.2	-12.8
S4	0	-12.8
S2	-12.8	-12.8
G2	-16	-12.8

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IMPORTANT NOTICE:

This product data sheet describes the characteristics of this product for which a warranty is granted. Any such warranty is granted exclusively under the terms and conditions of the supply agreement. There will be no guarantee or of any kind for the product and its characteristics.

The data contained in this document is exclusively intended for technically trained staff. You and your technical departments will have to evaluate the product's suitability for the intended application and the completeness of the product data concerning such application.

Due to technical requirements, our product may contain dangerous substances. For information on the types in question, please contact the sales staff responsible for you.

Changes to this product data sheet are reserved.

Please contact the sales staff (sales@hiitio.com) for further information on the product, technology, delivery terms, conditions and prices.

Instruction note

Naming rules for power module product models (Industrial module)

Product Model							
	HC	S	36	FS	140	E0	Q1
Hecheng Code							
Module type G : IGBT module D : FRD module S : SiC module H : Si/SiC hybrid							
On-state resistance (mΩ) 01~80							
Topology structure FZ : A switch unit FF : Half bridge FS : Three phase F4 : H Bridge F3L : Three level DF : Boost Circuit FD : Braking Circuit FP : Rectification+Inverter+Control move AL : ANPC CL : Chopper							
Voltage level (x10) (V) 650~2200							
Packaging form+features (A...Z)							
A1: 34 mm		A2: 62 mm					
B1: Easy 1B		B1A		B1B...			
B2: Easy 2B...		B3: Easy 3B...					
D1: Flow0		D2: Flow1		D3: Flow2			
E0 : E0		E1: Econo 2...		E2: E2			
E3: ED3		E4 : E4		E5 : ED3S			
E6 : EPM2		E7 : EPM3		E8 : EconoPIM3			
E9 : ED3H		F0 : F0		P2 : EPM2			
Feature :A: Special Code Nil: Standard							

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